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Voice over IP



Upkar Varshney, Andy Snow, Matt McGivern, Christi Howard January 2002 Communications of the ACM, Volume 45 Issue 1

Publisher: ACM Press

Full text available: pdf(113.77 KB) html(34.89 KB)

Additional Information: full citation, abstract, references, index terms

How can voice over the Internet claim a greater share of the worldwide phone market from the voice infrastructure dominated for more than 100 years by the public-switched telephone network?

2 Voice over IP: A conference gateway supporting interoperability between SIP and



H.323

Jiann-Min Ho, Jia-Cheng Hu, Peter Steenkiste

October 2001 Proceedings of the ninth ACM international conference on Multimedia **Publisher: ACM Press**

Full text available: pdf(805.09 KB)

Additional Information: full citation, abstract, references, citings, index terms

Increased network bandwidth is making desktop video conferencing an attractive application for an increasing number of computer users. Unfortunately, two competing standards for video conferencing signaling are in use, H.323 and SIP. In this paper we look at the interoperability between these two standards by developing a conferencing gateway that supports conferences involving both SIP and H.323 clients. By appropriately translating between H.323 and SIP operations, our prototype gateway suppor ...

Keywords: H.323, SIP, interoperability, video conferencing gateway, video conferencing signaling protocols

3 VoIP and Embedded Linux

September 2000 Linux Journal

Publisher: Specialized Systems Consultants, Inc.

Full text available: (a) html(9.15 KB) Additional Information: full citation, index terms 4 Towards junking the PBX: deploying IP telephony

Wenyu Jiang, Jonathan Lennox, Henning Schulzrinne, Kundan Singh

January 2001 Proceedings of the 11th international workshop on Network and operating systems support for digital audio and video

Publisher: ACM Press

Full text available: pdf(312.40 KB)

Additional Information: <u>full citation</u>, <u>abstract</u>, <u>references</u>, <u>citings</u>, <u>index</u> <u>lerms</u>

We describe the architecture and implementation of our Internet teleph ony test-bed intended to replace the departmental PBX (telephone switch). It interworks with the traditional telephone networks via a PSTN/IP gateway. It also serves as a corporate or campus infrastructure for existing and future services like web, email, video and streaming media. Initially intended for a few users, it will eventually replace the plain old telephones from our offices, due to the cost benefit and new ...

Keywords: PSTN/IP interoperability, SIP, VoIP test-bed, internet telephony deployment

5 Transporting voice traffic over packet networks

Larry Greenstein

July 1998 International Journal of Network Management, Volume 8 Issue 4

Publisher: John Wiley & Sons, Inc.

Full text available: pdf(201.37 KB) Additional Information: full citation, abstract, index terms

POTS networks are being rapidly superceded by newer, packet-based ones, which allows a greater facility for voice traffic. This article explores the practical issues involved in deploying voice networks over ATM, frame relay and IP. © 1998 by John Wiley & Sons, Ltd.

⁶ A decade of reconfigurable computing: a visionary retrospective

R. Hartenstein

March 2001 Proceedings of the conference on Design, automation and test in Europe

Publisher: IEEE Press

Full text available: pdf(768.00 KB) Additional Information: full citation, references, citings, index terms

7 Improving and managing multimedia performance over TCP-IP nets

Nathan J. Muller

December 1998 International Journal of Network Management, Volume 8 Issue 6

Publisher: John Wiley & Sons, Inc.

Full text available: pdf(338.34 KB) Additional Information: full citation, abstract, index terms

The TCP-IP-based Internet and, consequently corporate Intranets, were not designed for multimedia traffic. This article discusses the several ways of improving multimedia performance, finding that data compression techniques are no longer the most important factor. © 1998 John Wiley & Sons, Ltd.

Modeling heterogeneous network traffic in wavelet domain

Sheng Ma, Chuanyi Ji

October 2001 IEEE/ACM Transactions on Networking (TON), Volume 9 Issue 5

Publisher: IEEE Press

Full text available: pdf(375.00 KB)

Additional Information: <u>full citation</u>, <u>abstract</u>, <u>references</u>, <u>citings</u>, <u>index</u> terms

Heterogeneous network traffic possesses diverse statistical properties which include complex temporal correlation and non-Gaussian distributions. A challenge to modeling

heterogeneous traffic is to develop a traffic model which can accurately characterize these statistical properties, which is computationally efficient, and which is feasible for analysis. This work develops wavelet traffic models for tackling these issues. In specific, we model the wavelet coefficients rather than the original t ...

Keywords: Long-range dependence, network traffic modeling, self-similiar traffic, wavelets

9	Assessing network infrastructure	
	Jim Serenbetz	
₩.	,	
	Publisher: ACM Press Full text available: pdf(285.25 KB) Additional Information: full citation, index terms	
	Tall Cox available. [Salpanias Ital) Tradition Italian Station, mask terms	
10	Kernel Korner: Linux Teleconferencing: Improving the Wireless Network	÷.
	Izzet Agoren May 2001 Linux Journal	
	Publisher: Specialized Systems Consultants, Inc.	
	Full text available: [4] html(15,00 KB) Additional Information: full citation, index terms	
		_
11	Special issue on wireless extensions to the internet: Fast handovers and context	<u></u>
	transfers in mobile networks Rajeev Koodli, Charles E. Perkins	
	October 2001 ACM SIGCOMM Computer Communication Review, Volume 31 Issue 5	
	Publisher: ACM Press	
	Full text available: pdf(1.16 MB) Additional Information: full citation, abstract, references	
	We describe recent work enabling fast handovers and context transfer between access routers offering Internet connectivity for mobile (often wireless) nodes. We present our	
	framework for engineering general context transfer solutions, and a protocol which uses	
	the framework to provide a simple yet general mechanism for carrying out context transfers during handovers. Since our mechanism operates at the network level, we	
	expect that it will be the most expedient way to provide for seamless han	
	Voywords: IDv6 context transfer fact handever mobile ID mobile network	
	Keywords : IPv6, context transfer, fast handover, mobile IP, mobile network	
40		_
12 W.:	Application-layer mobility using SIP Henning Schulzrinne, Elin Wedlund	<u></u>
	July 2000 ACM SIGMOBILE Mobile Computing and Communications Review, Volume 4	
	Issue 3 Publisher: ACM Press	
	Full text available: pdf(1.34 MB) Additional Information: full citation, abstract, citings, index terms	
	Supporting mobile Internet multimedia applications requires more than just the ability to	
	maintain connectivity across subnet changes. We describe how the Session Initiation	
	Protocol (SIP) can help provide terminal, personal, session and service mobility to applications ranging from Internet telephony to presence and instant messaging. We also	

briefly discuss application-layer mobility for streaming multimedia applications initiated by

RTSP.

13	Special issue on wireless extensions to the internet: Interworking internet telephony and wireless telecommunications networks Jonathan Lennox, Kazutaka Murakami, Mehmet Karaul, Thomas F. La Porta October 2001 ACM SIGCOMM Computer Communication Review, Volume 31 Issue 5 Publisher: ACM Press	
	Full text available: pdf(1.09 MB) Additional Information: full citation, abstract, references	
	Internet telephony and mobile telephony are both growing very rapidly. Directly interworking the two presents significant advantages over connecting them through an intermediate PSTN link. We propose three novel schemes for the most complex aspect of the interworking: call delivery from an Internet telephony (SIP) terminal to a mobile telephony (UMTS) terminal. We then evaluate the proposals both qualitatively and quantitatively. However, existing equipment may not support packet interfaces n	
14	Mobile IP and the IETF Charles E. Perkins July 2000 ACM SIGMOBILE Mobile Computing and Communications Review, Volume 4 Issue 3 Publisher: ACM Press	
	Full text available: pdf(645.70 KB) Additional Information: full citation, index terms	
A 100	The case for services over cascaded networks Anthony D. Joseph, B. R. Badrinath, Randy H. Katz October 1998 Proceedings of the 1st ACM international workshop on Wireless mobile	
	multimedia Publisher: ACM Press	
	Full text available: pdf(1.08 MB) Additional Information: full citation, references, citings, index terms	
16	From voice-band modems to DSL technologies September 2001 International Journal of Network Management, Volume 11 Issue 5 Publisher: John Wiley & Sons, Inc.	
	Full text available: pdf(170.80 KB) Additional Information: full citation, abstract, references, index terms, review	
	This paper provides an overview of the evolution of digital transmission in the copper access network from voice-band modems to Digital Subscriber Line (DSL) technologies. The various types of DSL technology are described. Copyright © 2001 John Wiley & Sons, Ltd.	
	Load chracterization and anomaly detection for voice over IP traffic Michel Mandjes, Iraj Saniee, Alexander Stolyar June 2001 ACM SIGMETRICS Performance Evaluation Review, Proceedings of the 2001 ACM SIGMETRICS international conference on Measurement and modeling of computer systems SIGMETRICS '01, Volume 29 Issue 1 Publisher: ACM Press	
	Full text available: pdf(191.14 KB) Additional Information: full citation, abstract, references	
	We consider the problem of traffic anomaly detection in IP networks. Traffic anomalies arise when there is overload due to failures in a network. We present general formulae for the variance of the cumulative traffic over a fixed time interval and show how the derived analytical expression simplifies for the case of voice over IP traffic, the focus of this paper. To detect load anomalies, we show it is sufficient to consider cumulative traffic over relatively long intervals such as 5 minutes. Th	

 $\textbf{Keywords:} \ \, \textbf{SNMP-based load characterization, VoIP traffic anomaly detection, variance estimation}$

10	Consider to CoC in ad has and inferentiatives based wireless nativaries: A software	_						
Session I: QoS in ad hoc and infra-structure based wireless networks: A software approach to channel-state dependent scheduling for wireless LANs Hiroto Aida, Yoshito Tobe, Masato Saito, Hideyuki Tokuda July 2001 Proceedings of the 4th ACM international workshop on Wireless mobile multimedia Publisher: ACM Press								
	Full text available: pdf(1.09 MB) Additional Information: full citation, abstract, references, index terms							
	This paper proposes a new practical packet scheduling scheme for wireless LANs called SoCPS and also explores extensively the effectiveness of wireless packet scheduling under a variety of conditions over an actual wireless LAN. One well-known aspect in packet scheduling at the base station is consideration of channel state dependency. In conventional studies, the channel state is expressed in microscopic two states in the Markovian sense. To eliminate a large overhead associated with acquiring							
19	Papers: Voice over IP performance monitoring							
	R. G. Cole, J. H. Rosenbluth							
~	R. G. Cole, J. H. Rosenbluth April 2001 ACM SIGCOMM Computer Communication Review, Volume 31 Issue 2							
	Publisher: ACM Press Full text available: pdf(1.35 MB) Additional Information: full citation, abstract, references, citings							
	We describe a method for monitoring Voice over IP (VoIP) applications based upon a reduction of the ITU-T's E-Model to transport level, measurable quantities. In the process, 1) we identify the relevant transport level quantities, 2) we discuss the tradeoffs between placing the monitors within the VoIP gateways versus placement of the monitors within the transport path, and 3) we identify several areas where further work and consensus within the industry are required. We discover that the releva							
20	A quick check of network performance Jeffrey T. Hicks, John Q. Walker January 2001 International Journal of Network Management, Volume 11 Issue 1 Publisher: John Wiley & Sons, Inc. Full text available: pdf(372.57 KB) Additional Information: full citation, abstract, references, index terms							
	Have you ever tried to measure the response time across a network? Do you sometimes wonder what throughput rate you're getting over a particular link? Are you concerned about the impact of adding streaming multimedia traffic to a network? Would you like to know the exact route your data is taking? Individual tools are available to measure the throughput and response time of your applications, trace a network route, or test a network's capacity for handling							
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UTLB: a mechanism for address translation on network interfaces

Yuqun Chen, Angelos Bilas, Stefanos N. Damianakis, Cezary Dubnicki, Kai Li

October 1998 ACM SIGPLAN Notices, ACM SIGOPS Operating Systems Review, Proceedings of the eighth international conference on Architectural support for programming languages and operating systems ASPLOS-

VIII, Volume 33, 32 Issue 11, 5

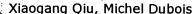
Publisher: ACM Press

Full text available: mpdf(1.76 MB)

Additional Information: full citation, abstract, references, citings, index terms

An important aspect of a high-speed network system is the ability to transfer data directly between the network interface and application buffers. Such a direct data path requires the network interface to "know" the virtual-to-physical address translation of a user buffer, i.e., the physical memory location of the buffer. This paper presents an efficient address translation architecture, User-managed TLB (UTLB), which eliminates system calls and device interrupts from the common co ...

Options for dynamic address translation in COMAs



April 1998 ACM SIGARCH Computer Architecture News, Proceedings of the 25th annual international symposium on Computer architecture ISCA '98, Volume 26 Issue 3

Publisher: IEEE Computer Society, ACM Press

Publisher Site

Full text available: pdf(1,37 MB) Additional Information: full citation, abstract, references, citings, index

In modern processors, the dynamic translation of virtual addresses to support virtual memory is done before or in parallel with the first-level cache access. As processor technology improves at a rapid pace and the working sets of new applications grow insatiably the latency and bandwidth demands on the TLB (Translation Lookaside Buffer) are getting more and more difficult to meet. The situation is worse in multiprocessor systems, which run larger applications and are plagued by the TLB consiste ...

High-bandwidth address translation for multiple-issue processors

Todd M. Austin, Gurindar S. Sohi

May 1996 ACM SIGARCH Computer Architecture News, Proceedings of the 23rd annual international symposium on Computer architecture ISCA '96. Volume 24 Issue 2

Publisher: ACM Press

Full text available: pdf(1.56 MB)

Additional Information: <u>full citation</u>, <u>abstract</u>, <u>references</u>, <u>citings</u>, <u>index</u> terms

In an effort to push the envelope of system performance, microprocessor designs are continually exploiting higher levels of instruction-level parallelism, resulting in increasing bandwidth demands on the address translation mechanism. Most current microprocessor designs meet this demand with a multi-ported TLB. While this design provides an excellent hit rate at each port, its access latency and area grow very quickly as the number of ports is increased. As bandwidth demands continue to increase ...

4 Translation hint buffers to reduce access time of physically-addressed instruction



caches

Brian K. Bray, Michael J. Flynn

December 1992 ACM SIGMICRO Newsletter, Proceedings of the 25th annual international symposium on Microarchitecture MICRO 25, Volume 23 Issue

Publisher: IEEE Computer Society Press, ACM Press

Full text available: pdf(504.04 KB) Additional Information: full citation, references, citings, index terms

Keywords: cache, physical address, translation lookaside buffer, virtual address, virtual memory

5 Eliminating the address translation bottleneck for physical address cache



Tzi-cker Chiueh, Randy H. Katz

September 1992 ACM SIGPLAN Notices, Proceedings of the fifth international conference on Architectural support for programming languages and operating systems ASPLOS-V, Volume 27 Issue 9

Publisher: ACM Press

Full text available: mpdf(1.28 MB)

Additional Information: full citation, references, citings, index terms

⁶ An in-cache address translation mechanism



D. A. Wood, S. J. Eggers, G. Gibson, M. D. Hill, J. M. Pendleton

June 1986 ACM SIGARCH Computer Architecture News, Proceedings of the 13th annual international symposium on Computer architecture ISCA '86, Volume 14 Issue 2

Publisher: IEEE Computer Society Press, ACM Press

Full text available: pdf(770.30 KB)

Additional Information: full citation, abstract, references, citings, index terms

In the design of SPUR, a high-performance multiprocessor workstation, the use of large caches and hardware-supported cache consistency suggests a new approach to virtual address translation. By performing translation in each processor's virtually-tagged cache, the need for separate translation lookaside buffers (TLBs) is eliminated. Eliminating the TLB substantially reduces the hardware cost and complexity of the translation mechanism and eliminates the translation consistency problem. Trac ...

7 Architectural support for translation table management in large address space



<u>machines</u>

Jerry Huck, Jim Hays

May 1993 ACM SIGARCH Computer Architecture News, Proceedings of the 20th annual international symposium on Computer architecture ISCA '93, Volume 21 Issue 2

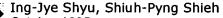
Publisher: ACM Press

Full text available: pdf(1.34 MB)

Additional Information: full citation, abstract, references, citings, index terms

Virtual memory page translation tables provide mappings from virtual to physical addresses. When the hardware controlled Translation Lookaside Buffers (TLBs) do not contain a translation, these tables provide the translation. Approaches to the structure and management of these tables vary from full hardware implementations to complete software based algorithms. The size of the virtual address space used by processes is rapidly growing beyond 32 bits of address. As the utilized ad ...

8 Virtual address translation for wide-address architectures



October 1995 ACM SIGOPS Operating Systems Review, Volume 29 Issue 4

Publisher: ACM Press

Full text available: pdf(569.30 KB) Additional Information: full citation, abstract, index terms

Operating systems employ virtual memory mechanism to provide large address space for programs. The efficiency of the virtual address translation plays an important role in determining system performance. In conventional virtual memory management systems, both the forward-mapped page table scheme and inverted page table scheme are widely used to organize the page tables that record translation data. These two schemes work well for 32-bit architectures, but not for wide address (64-bit) architectu ...

The TLB slice—a low-cost high-speed address translation mechanism



May 1990 ACM SIGARCH Computer Architecture News , Proceedings of the 17th annual international symposium on Computer Architecture ISCA '90, Volume 18 Issue 3a

Publisher: ACM Press

Additional Information: full citation, abstract, references, citings, index Full text available: pdf(731.80 KB) terms

The MIPS R6000 microprocessor relies on a new type of translation lookaside buffer called a TLB slice — which is less than one-tenth the size of a conventional TLB and as fast as one multiplexer delay, yet has a high enough hit rate to be practical. The fast translation makes it possible to use a physical cache without adding a translation stage to the processor's pipeline. The small size makes it possible to include address translation on-chip, even in a tech ...

Binary translation and architecture convergence issues for IBM system/390



May 2000 Proceedings of the 14th international conference on Supercomputing

Publisher: ACM Press

Full text available: Todf(1.44 MB) Additional Information: full citation, abstract, references, index terms

We describe the design issues in an implementation of the ESA/390 architecture based on binary translation to a very long instruction word (VLIW) processor. During binary translation, complex ESA/390 instructions are decomposed into instruction "primitives" which are then scheduled onto a wide-issue machine. The aim is to achieve high instruction level parallelism due to the increased scheduling and optimization opportunities which can be exploited by binary translation software ...

11 Performance of the VAX-11/780 translation buffer: simulation and measurement

Douglas W. Clark, Joel S. Emer

February 1985 ACM Transactions on Computer Systems (TOCS), Volume 3 Issue 1

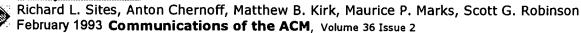
Publisher: ACM Press



Additional Information: full citation, abstract, references, citings, index Full text available: 7 pdf(2.36 MB) terms

A virtual-address translation buffer (TB) is a hardware cache of recently used virtual-tophysical address mappings. The authors present the results of a set of measurements and simulations of translation buffer performance in the VAX-11/780. Two different hardware monitors were attached to VAX-11/780 computers, and translation buffer behavior was measured. Measurements were made under normal time-sharing use and while running reproducible synthetic time-sharing work loads. Reported measure ...

12 Binary translation



Publisher: ACM Press

Full text available: pdf(4.84 MB) Additional Information: full citation, references, citings, index terms

Keywords: CISC computers, RISC computers, binary translation, computer architecture, processor architecture translation

13 Machine-adaptable dynamic binary translation

David Ung, Cristina Cifuentes

January 2000 ACM SIGPLAN Notices, Proceedings of the ACM SIGPLAN workshop on Dynamic and adaptive compilation and optimization DYNAMO '00, Volume

Publisher: ACM Press

Additional Information: full citation, abstract, references, citings, index Full text available: pdf(1.23 MB) terms

Dynamic binary translation is the process of translating and optimizing executable code for one machine to another at runtime, while the program is "executing" on the target machine.

Dynamic translation techniques have normally been limited to two particular machines; a competitor's machine and the hardware manufacturer's machine. This research provides for a more general framework for dynamic translations, by providing a framework based on specifications of machines that ...

Keywords: binary translation, dynamic compilation, dynamic execution, emulation, interpretation

14 Philco/IBM translation at problem-oriented, symbolic and binary levels

Thomas M. Olsen

December 1965 Communications of the ACM, Volume 8 Issue 12

Publisher: ACM Press

Full text available: pdf(947,25 KB) Additional Information: full citation, references, index terms

15 Communications between independently translated blocks

Peter Wegner July 1962 Communications of the ACM, Volume 5 Issue 7

Publisher: ACM Press

Full text available:





Additional Information: full citation, references

16 Special section: Machine translation of natural languages



April 1985 ACM SIGART Bulletin, Issue 92

Publisher: ACM Press

Full text available: pdf(1.75 MB)

Additional Information: full citation, abstract, references

The field of machine translation has recently entered a new, third period in its evolution. In its early period, for roughly fifteen years from 1950 MT was an expanding field of study in which both research and development efforts were undertaken. It is well-known and well documented (Bar Hillel, 1960; ALPAC, 1966) that this early MT paradigm could not and did not produce fully automated high quality translation systems. In fact, the practical results were quite negligible for such a high-scale ...

17 Machine translation: Session summary

Alex Waibel

March 1993 Proceedings of the workshop on Human Language Technology HLT '93

Publisher: Association for Computational Linguistics

Full text available: pdf(282.21 KB) Additional Information: full citation, abstract

Machine Translation was one of the declared highlights and focal points of the Human Language Technology Workshop. Machine Translation, or MT for short, has seen a renaissance in recent years, brought about by the availability of faster and more powerful computing, and several decades of advances in speech and language processing. ARPA now sponsors a machine translation initiative and companies and governments, domestic and overseas, view it as a growth area and devote significant efforts to thi ...

18 On the correct translation of update operations on relational views

Umeshwar Dayal, Philip A. Bernstein

September 1982 ACM Transactions on Database Systems (TODS), Volume 7 Issue 3

Publisher: ACM Press

Full text available: pdf(2.55 MB)

Additional Information: full citation, abstract, references, citings, index terms

Most relational database systems provide a facility for supporting user views. Permitting this level of abstraction has the danger, however, that update requests issued by a user within the context of his view may not translate correctly into equivalent updates on the underlying database. The purpose of this paper is to formalize the notion of update translation and derive conditions under which translation procedures will produce correct translations of view updates.

Keywords: external schemata, relational databases, schema mapping, update translation, user views

19 Optimizations and oracle parallelism with dynamic translation

Kemal Ebcioğlu, Erik R. Altman, Michael Gschwind, Sumedh Sathaye

November 1999 Proceedings of the 32nd annual ACM/IEEE international symposium on Microarchitecture

Publisher: IEEE Computer Society

Full text available: pdf(1.28 MB) Additional Information: full citation, abstract, references, citings, index terms Publisher Site

We describe several optimizations which can be employed in a dynamic binary translation (DBT) system, where low compilation/translation overhead is essential. These optimizations achieve a high degree of ILP, sometimes even surpassing a static compiler employing more sophisticated, and more time-consuming algorithms [9]. We present results in which we employ these optimizations in a dynamic binary translation system capable of computing oracle parallelism.

A virtual memory translation mechanism to support checkpoint and rollback recovery Nicholas S. Bowen, Dhiraj K. Pradhan



August 1991 Proceedings of the 1991 ACM/IEEE conference on Supercomputing

Publisher: ACM Press

Full text available: pdf(936.90 KB) Additional Information: full citation, references, citings, index terms

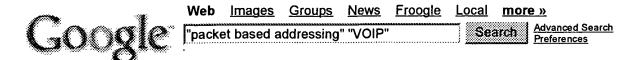
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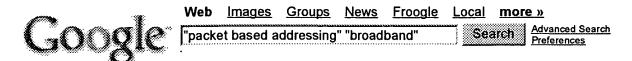
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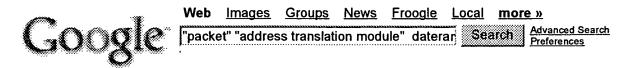
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screenos5x/screenos5xmaintenance/m_5.0.0_r9_RevB.pdf - Supplemental Result - Similar pages

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4 - (length % 4) : 0)); + if (!packet) + return NULL; + + if (length % 4) { /* zero padding bytes */ + packet->data[length >> 2] = 0; + } + packet->host ... www.kadlec.lkams.kernel.org/pub/linux/ kernel/v2.6/snapshots/incr/patch-2.6.0-test4-bk3-bk4 - 529k - Supplemental Result - Cached - Similar pages

diff -Nru linux-2.4.13-plain/net/ipv4/netfilter/Config.in linux ...

... +MODULE_DESCRIPTION("IRC (DCC) network address translation Module"); ... + if (net_ratelimit()) { + printk + ("IRC_NAT: partial packet %u/%u in %u/%u\n" ... svn.netliter.org/.../old_stuff/netfilter/ tags/iptables_1_2_5/patch-o-matic/submitted/2.4.14.patch?rev=3069 - 85k - Supplemental Result - Cached - Similar pages

diff -wNBur --strip-trailing-cr --ignore-matching-lines='^.*Id ...

diff -wNBur -strip-trailing-cr -ignore-matching-lines='^.*Id:.

*\$' /work2/hardhat/previewkit/kernel/linux-2.4.17_mvl21/fs/proc/base.c ...

wwwiap.physik.uni-giessen.de/.../fritzbox/ packages/linux/fritzbox-avm-kernel-

2.4.17/mvl21+iw+hrt_avm3.diff - Supplemental Result - Similar pages

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What algorithm does software use to translate protocol address into a hardware address? a computer may need to use more than one address translation module. ... neptune.netcomp.monash.edu.au/cpe3004/ lecture/PreparatoryQuestions/Questions-L4-3004.doc - Supplemental Result - Similar pages

3c574_cs 3Com 3c574 series PCMCIA ethernet driver 3c589_cs 3Com ...

... module ip_nat_irc IRC (DCC) Network address translation module ip_nat_snmp_basic Basic SNMP Application layer Gateway ip_queue IPv4 Packet queue handler ... anonen.csc.warwick.ac.uk/mirrors/Mandrakelinux/

devel/cooker/ppc/install/stage2/live/lib/modules.description - 5k - Supplemental Result - Cached - Similar pages

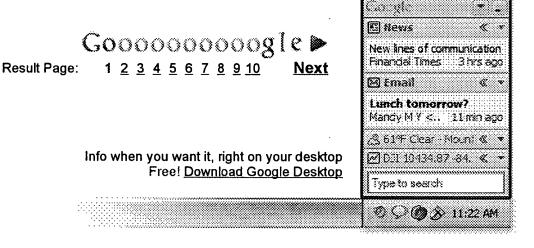
diff -u --recursive --new-file --exclude=CVS --exclude=.cvsignore ...

If in doubt, say N. + +IP6 tables support (required for filtering/masq/NAT)
+CONFIG_IP6_NF_IPTABLES + ip6tables is a general, extensible packet ...
www.linuxgrill.com/anonymous/ kernel/Archive/DaveM/zerocopy-2.4.0-1.diff.gz - 597k Supplemental Result - Cached - Similar pages

diff -Nru a/CREDITS b/CREDITS --- a/CREDITS Tue Mar 18 08:47:34 ...

-The third and fourth column are respectively The **algorithm** and adapter -name of ... each adapter is associated with an **algorithm**, -and several adapters can ... ftp.megamirror.com/pub/linux/ kernel/v2.5/snapshots/old/patch-2.5.66-bk10.gz - Supplemental Result - <u>Similar pages</u>

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